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# Faculty of Engineering & Technology

# Eighth Semester B.E. (Electronics)/EDT Examination

# EMBEDDED SYSTEMS

### Elective-II

# Sections-A & B

Time: Three Hours] [Maximum Marks: 80

## INSTRUCTIONS TO CANDIDATES

- (1) All questions carry marks as indicated.
- (2) Answer **THREE** questions from Section—A and **THREE** questions from Section—B.
- (3) Due credit will be given to neatness and adequate dimensions.
- (4) Illustrate your answers wherever necessary with the help of neat sketches.

### SECTION-A

- Draw the block diagram of 68HC12 and explain the on-chip peripherals in detail.
- Interface 16×1 liquid crystal display to 68HC12. Also write and explain the WHY P program to display 'Embedded systems' on LCD.

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3.	(a) (b)	What are different addressing modes of 68HC12? Explain with one example each. 7  Describe different hardware interrupts of 68HC12.			8.	Explain with block diagram Analog to Digital converter module of 68HC12. Comment on the following terms:				
http://www.rtmnuonline.com		List them according to priorities.	6			(i)	8/4 channel mode			
	(a)	Explain the system stack and data stack in Give an example.	7	http://www.		(iii)	continuous scan control multiple/single channel channel addresses	http://www.ri		
	(b)	What the WHY P words MIN and MA Write the WHY P code for the same.	7	T I		(v)	sequence complete flag		mnuoi	
	Inte			uonline.			conversion complete flag.	13		
	MC 14499 decoder driver with 68HC12 and write WHY P program to display 0000 to 9999 count with the interval of 1 sec. Discuss the program in brief. 13  SECTION—B		.com	9.	done using 68HC12.	Explain the pulse accumulator available in ti	7	.com		
6. http://www.rtmnuonline.com	(a)	Explain the memory mapping of 68HC12	. 7		10.	(a)	Explain the words :—			
	(b)	Discuss the out compare and input ca 68HC12.	apture in 7	http://wwv			(i) SIGN (ii) TYPE		http://www.	
	(a) (b)	Explain in detail :  (i) ATDCTLS		v.rtmnuon			(iii) SPACE (iv) HOLD.	7	rtmnuon 7	
		(ii) ATDSTAT (iii) ATDSTATL.	9	line.com		(b)	Explain the following instructions of 68HC12  (i) EMVL	! :—	line.com	
		Discuss data register and baud rate control re of SCI.	ol register 4				(ii) FDIV (iii) LDD 2, X —.	6	6	
			7977				(m) LDD 2, A —.	U		
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