Faculty of Engineering & Technology Fourth Semester B.E. (Computer Science Engineering) (C.B.S.) Examination OPERATING SYSTEM Paper–III

	I ime—I	hree Hours]	[Max	ximum Marks—80		
	INSTRUCTIONS TO CANDIDATES					
	(1)	All questions carry marks as indicated.				
	(2)	Assume suitable	data wherever	necessary.		
	(3)	Illustrate your answers wherever necessary with				
		the help of neat d		•		
	1. (a) What is Operating System? Explain Batch and Distributed Operating System.					
	(a)			7		
	(b) Explain the various Hardware requirements for Modern					
		Operating System	l. -	6		
			OR			
	2. (a)	List and explain va system.	rious services pr	rovided by operating		
	(b)	What is system ca	all ? Explain it	s categories. 6		
	3. (a)	Define a file sy	ystem. What	are the differen		
		components of a		6		
	(p)	Explain the differ	ent access met	hods for file. 7		
	OR					
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- 4. (a) Explain contiguous allocation and index allocation method.
 - (b) Suppose the head of moving head disk with 200 tracks is currently at track 60. If the queue of a request is kept in order as 65,170,35,120,10,140 what is the total read movement to satisfy the request for the scheme?
 - (i) FCFS
 - (ii) CSCAN
 - (iii) SSTF
 - (iv) LOOK

7

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- 5. (a) Draw the state transition diagram of a process and explain each state in brief.
 - (b) What are the different kinds of schedules present in the system? Bring out the relevance of each of them.

OR

- 6. (a) Define thread. Explain various multi threading models for thread.
 - (b) Consider the following set of processes.

Process	Cpu Burst	Arrival
	time	time
P	3	0
P,	5	1
P ₁	2	2
P	5	3
P.	5	4

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Contd.

Calculate the average waiting time and Turnaround time for each algorithm.

- (i) FCFS
- (ii) SJF
- (iii) RR (time slice =2)
- 7. (a) Explain the difference between internal and external fragmentation.
 - (b) Explain the hardware implementation of paging in detail with neat sketch and example.7

OR

- 8. (a) A page size is always a power of 2. Justify the statement.
 - (b) Implementation of virtual memory is architecture dependent. Comment. 4
 - (c) Consider the following page reference string
 1, 2, 3, 4, 1, 2, 5, 1, 2, 3, 4, 5 for a memory with
 3 frames. How many page faults would occur for following page replacement algorithm?
 - (i) LRU replacement
 - (ii) FIFO replacement
 - (iii) Optimal replacement.

9. (a) What is mutual Exclusion? How are semaphores used to solve the critical section problem? 7

Contd.

7

	(b)	What is semaphore? What is the difference betw	een	
		a binary and counting semaphore?	7	
		OR		
10.	(a)	Describe the producer consumer bounded but	ffer	
		problem. Give routines for producer and consum	mer	
		process.	7	
	(b)	What are P and V operations in proc	ess	
the state of the state of		synchronization? Explain.	7	
11.	(a)	What are necessary conditions for occurrence	e of	
		deadlock and how deadlock can be prevented	1?	
-			7	
	(b)	Describe the resource allocation graph. Prove th	at if	
		a graph contain a cycle still there is no deadlo	ck.	
			6	
		OR		
12.	(a)	Explain the access matrix in detail along with	h its	
		implementation.	4	
	(b)	Compare Capability list with Access list.	3	
	(c)	Write short notes on (any two):		
		(i) Cryptography		
		(ii) Security threat		
		(iii) Recovery from Dead Lock.	6	